

	Type	Hits	Search Text	DBs
1	BRS	8685	single adj integrated adj circuit	USPAT
2	BRS	0	(single and disjoint) adj integrated adj circuit	USPAT
3	BRS	414	(single and (disjoint or separat\$3 or disconnected)) adj integrated adj circuit	USPAT
4	BRS	0	((single and (disjoint or separat\$3 or disconnected)) adj integrated adj circuit) same controller same subsystem	USPAT
5	BRS	6	((single and (disjoint or separat\$3 or disconnected)) adj integrated adj circuit) same controller and subsystem	USPAT
6	BRS	57	((single and (disjoint or separat\$3 or disconnected)) adj integrated adj circuit) same controller	USPAT
7	BRS	195	712/36.ccls.	USPAT
8	BRS	131	712/37.ccls.	USPAT
9	BRS	27307	((single adj chip) or (intergated adj circuit))	USPAT; US-PG PUB; EPO; JPO; DERWE NT; IBM_T DB

	Type	Hits	Search Text	DBs
10	BRS	1053	((single adj chip) or (intergated adj circuit)) same (microcontroller or (subsystem adj controller))	USPAT ; US-PG PUB; EPO; JPO; DERWE NT; IBM_T DB
11	BRS	1050	((single adj chip) or (intergated adj circuit)) same (microcontroller)	USPAT ; US-PG PUB; EPO; JPO; DERWE NT; IBM_T DB
12	BRS	565	((single adj chip) or (intergated adj circuit)) near2(microcontroller)	USPAT ; US-PG PUB; EPO; JPO; DERWE NT; IBM_T DB
13	BRS	565	((single adj chip) or (intergated adj circuit)) near2 (microcontroller)	USPAT ; US-PG PUB; EPO; JPO; DERWE NT; IBM_T DB

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14	BRS	416	((single adj chip) or (intergated adj circuit)) adj (microcontroller)	USPAT ; US-PG PUB; EPO; JPO; DERWE NT; IBM_T DB
15	BRS	0	((single adj chip) or (intergated adj circuit)) adj (subsystem adj controller)	USPAT ; US-PG PUB; EPO; JPO; DERWE NT; IBM_T DB
16	BRS	41	((single adj chip) or (intergated adj circuit)) adj (microcontroller)) and ((electronic adj interface) or LCD)	USPAT ; US-PG PUB; EPO; JPO; DERWE NT; IBM_T DB
17	BRS	2	5588118.pn.	USPAT ; US-PG PUB; EPO; JPO; DERWE NT; IBM_T DB

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12	BRS	565	((single adj chip) or (intergated adj circuit)) near2(microcontroller)	USPAT ; US-PG PUB; EPO; JPO; DERWE NT; IBM_T DB
13	BRS	565	((single adj chip) or (intergated adj circuit)) near2 (microcontroller)	USPAT ; US-PG PUB; EPO; JPO; DERWE NT; IBM_T DB

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16	BRS	41	((single adj chip) or (intergated adj circuit)) adj (microcontroller)) and ((electronic adj interface) or LCD)	USPAT ; US-PG PUB; EPO; JPO; DERWE NT; IBM_T DB